

FACULDADE DE ENGENHARIA DA UNIVERSIDADE DO PORTO



**Development of a reconfigurable  
multi-protocol verification environment  
using UVM methodology**

**Pedro Araujo**

DISSERTATION  
PROGRESS REPORT 5

Mestrado Integrado em Engenharia Eletrotécnica e de Computadores

Teacher supervisor: José Carlos Alves

Company supervisors: Luis Cruz and Domingos Terra

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# Chapter 1

## Report

Work done:

- Structure of the final document of the thesis

Difficulties:

- Nothing to report

