

FACULDADE DE ENGENHARIA DA UNIVERSIDADE DO PORTO



**Development of a reconfigurable
multi-protocol verification environment
using UVM methodology**

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DISSERTATION
PROGRESS REPORT 4

Mestrado Integrado em Engenharia Eletrotécnica e de Computadores

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Chapter 1

Report

Work done:

- Development of the chosen approach to the testbench
- Start of the development of a DUT in order to demonstrate the testbench capabilities

Difficulties:

- Nothing to report

